

FIG. 4A

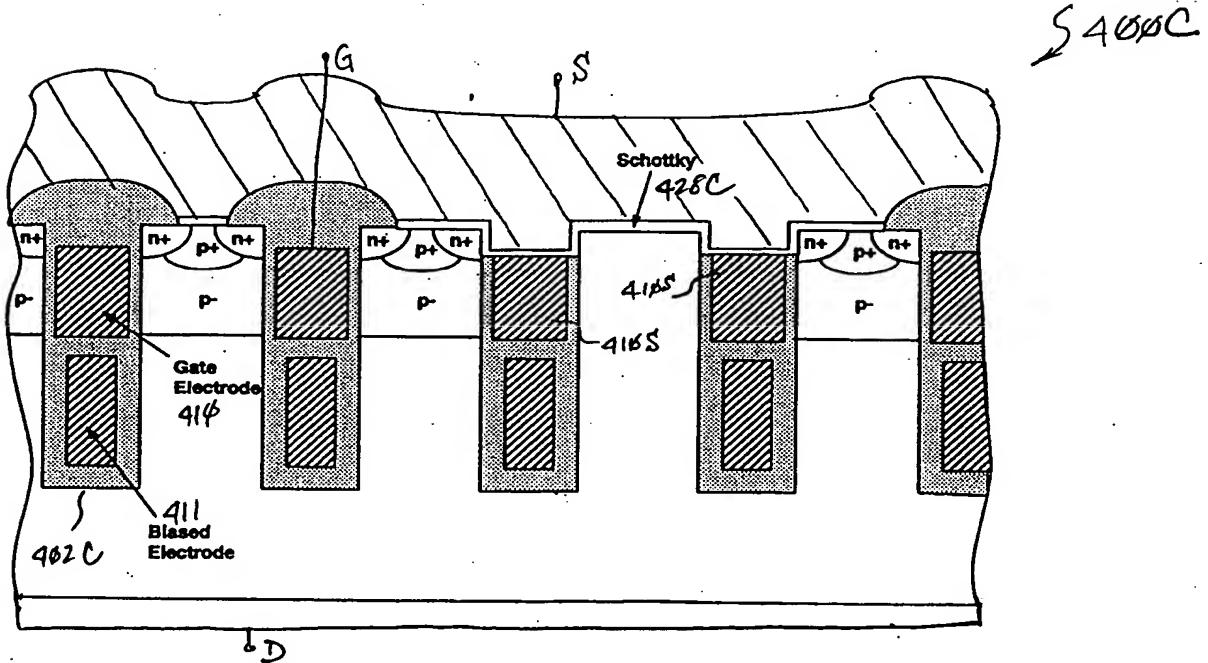


FIG. 4B

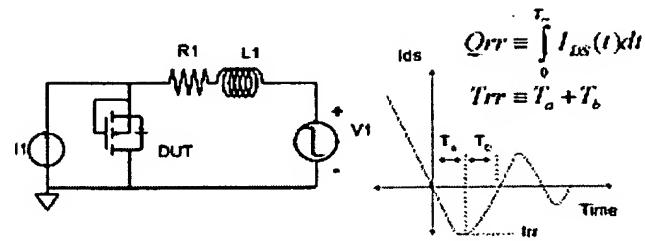


FIG. 5

Fig. 4. Mixed-Mode circuit and example waveform for Qrr simulations

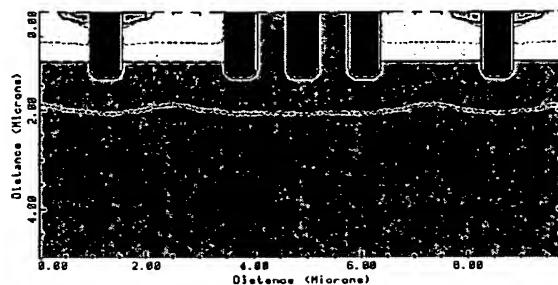


FIG. 6

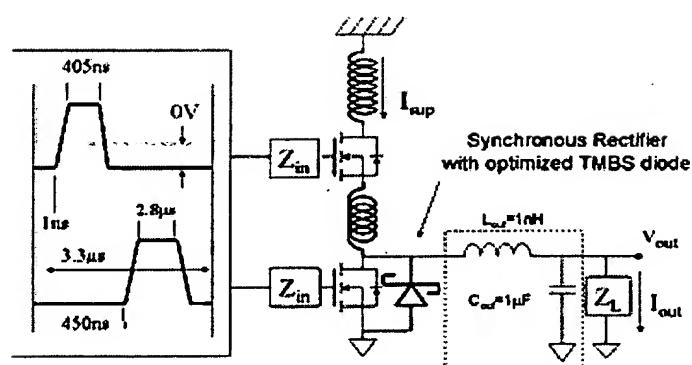


FIG. 7

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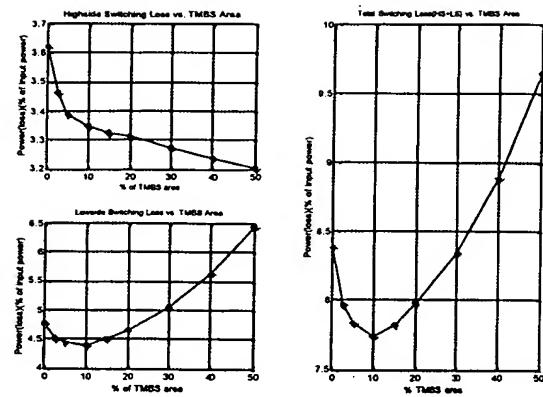


FIG. 8

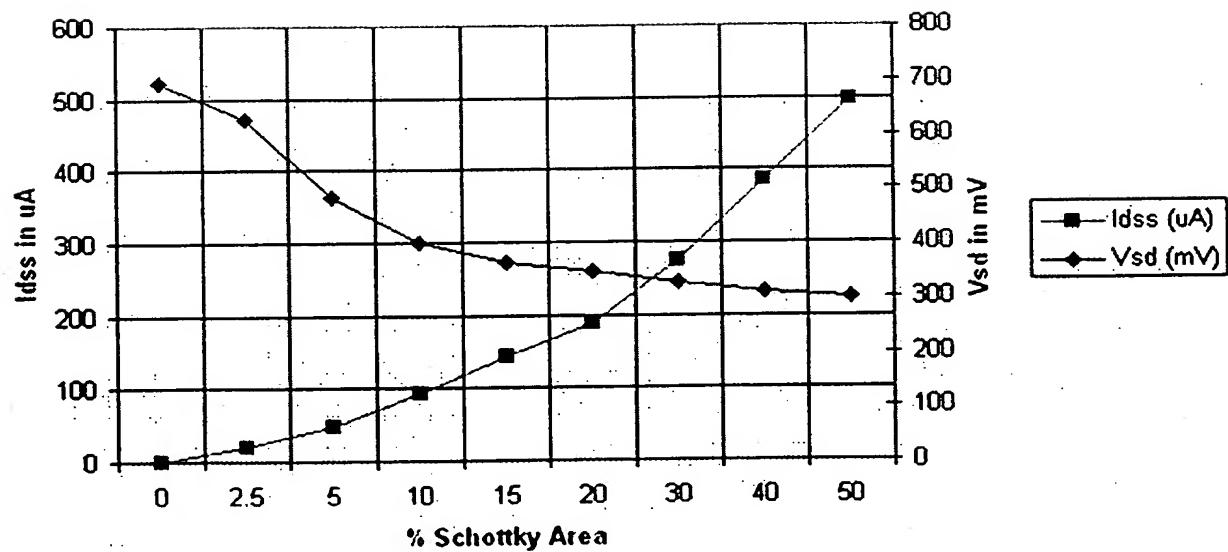


FIG. 9

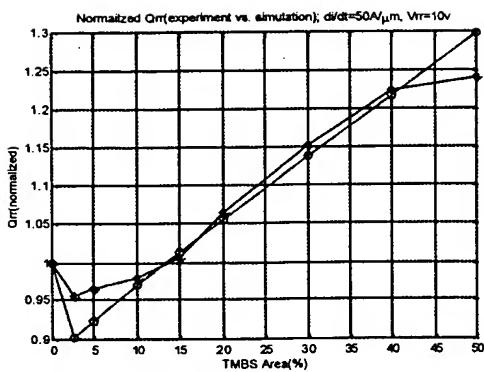


FIG. 10

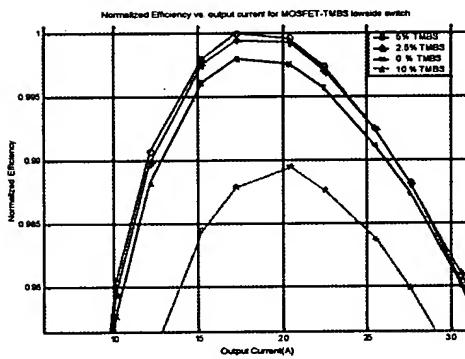


FIG. 11

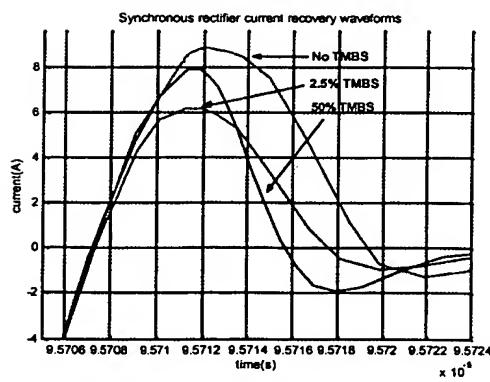


FIG. 12

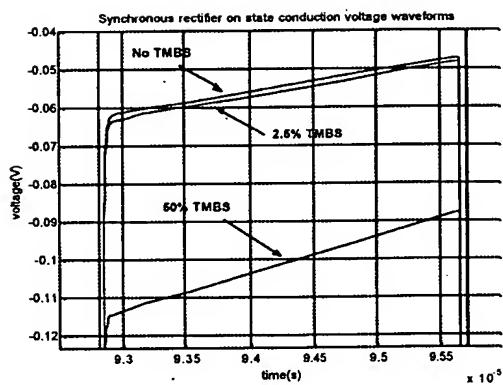


FIG. 13

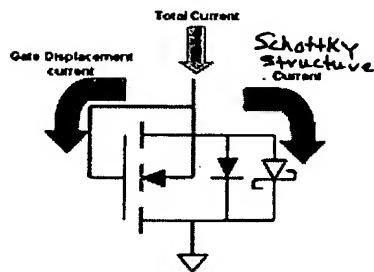


FIG. 14

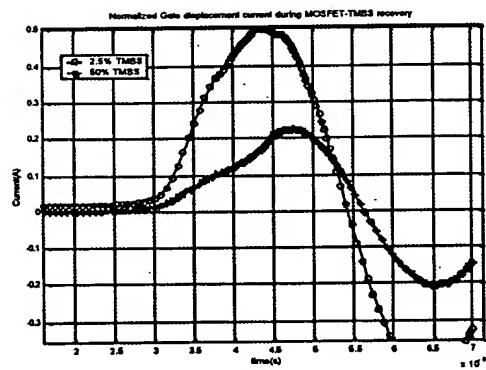


FIG. 15